ADCDS-1240

12-Bit, 40 Megapixels/Second
CCD Signal Processor

Preliminary Data

For full details go to
www.datel.com/rohs

FEATURES

- 40 MPPS
- Internal 12-bit resolution A/D
- Proprietary correlated double sampling
- Small, 40-pin, TDIP or SMT package
- Programmable Offset Adjustment
- Gain Adjustment Feature
- Low cost, functionally complete
- Low Power

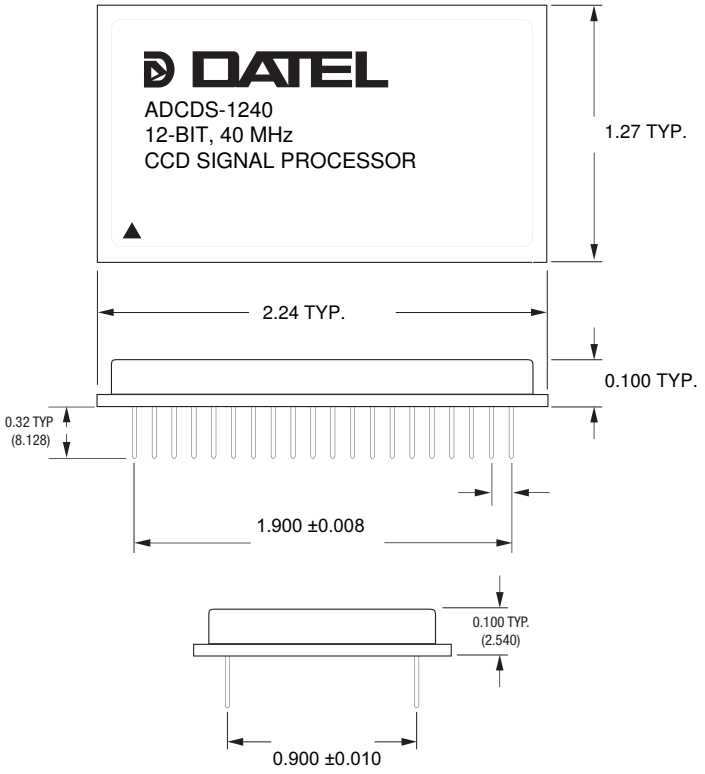
PRODUCT OVERVIEW

The ADCDS -1240 is an application -specific CCD signal processor designed for electronic-imaging applications that employ CCD's (charge coupled devices) as their photo detector. The ADCDS-1240 utilizes a proprietary architecture to convert at high pixel transfer rates without compromising system noise integrity.

To maximize dynamic range, this 12-bit CCD Signal Processor incorporates a digitally controlled calibration feature to null multiple CCD output offset errors, and an analog input front end that allows the user to make both gross and fine gain adjustments.

Using a high-speed, low-noise sampling A/D converter the ADCDS-1240 produces a 12-bit, parallel output, digitized representation of the video output of the CCD.

The ADCDS-1240 requires only a rising edge of the R_Conv pulse to capture the reference level of the CCD output and a rising edge of the P_Conv to initiate the conversion process. Timing circuitry can be configured such that a single clock operating at 2x the output data rate will serve as both the R_Conv and P_Conv signals.
Additional features include precision +10V reference and output data overflow flag.



TDIP
Package
(inches)

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